

IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A digital receiver comprising:
  - a tuner for converting a selected channel to a baseband channel signal;
  - an adjustable low pass filtering arrangement for filtering said baseband signal;
  - a demodulator including a first measuring arrangement for measuring a first value as a first function of a signal level of at least one first baseband signal component of said baseband signal whose frequency is adjacent an edge of said baseband channel and for measuring a second value as a second function of a signal level of at least one second baseband signal component of said baseband signal whose frequency is further from said edge of said baseband channel than a frequency of said at least one first baseband signal component, wherein said first value is measured as an average of levels of a first plurality of carriers whose frequencies are adjacent said edge of said baseband channel; and
  - a controller for adjusting a passband of said low pass filtering arrangement such that said first and second values have a predetermined relationship, wherein the demodulator outputs a channel signal within the passband of said low pass filtering arrangement.

2. (Original) A receiver as claimed in claim 1, in which said filtering arrangement is an analog filtering arrangement.
3. (Original) A receiver as claimed in claim 1, comprising an analog-digital conversion arrangement between said filtering arrangement and said first measuring arrangement.
4. (Original) A receiver as claimed in claim 1, in which said tuner is a single conversion zero intermediate frequency tuner.
5. (Original) A receiver as claimed in claim 1, in which said controller is arranged initially to adjust said filtering arrangement to have a smaller passband than a bandwidth of said baseband channel.
6. (Original) A receiver as claimed in claim 5, in which said controller is arranged to increment said passband of said filtering arrangement until said predetermined relationship exists.
7. (Canceled)
8. (Original) A receiver as claimed in claim 1, in which said first measuring arrangement is arranged to perform a Fourier transform.

9. (Original) A receiver as claimed in claim 8, in which said Fourier transform is a discrete Fourier transform.

10. (Original) A receiver as claimed in claim 8, in which said Fourier transform is a fast Fourier transform.

11. (Original) A receiver as claimed in claim 1, in which said selected channel is orthogonal frequency division multiplexed.

12. (Canceled)

13. (Currently Amended) A receiver as claimed in claim 11 ~~[[12]]~~, in which said carriers include at least one pilot carrier and said average is a weighted average.

14. (Currently Amended) A receiver as claimed in claim 11 ~~[[12]]~~, in which said average is formed over a plurality of symbols.

15. (Original) A receiver as claimed in claim 14, in which said symbols are consecutive symbols.

16. (Currently Amended) A receiver as claimed in claim 11 ~~[[12]]~~, in which said second value is measured as an average of levels of a second plurality of carriers.

17. (Original) A receiver as claimed in claim 16, in which said second plurality of carriers comprises all carriers.

18. (Original) A receiver as claimed in claim 16, in which said second plurality of carriers comprises all carriers except those of said first plurality.

19. (Original) A receiver as claimed in claim 16, in which said average is formed over a plurality of symbols.

20. (Original) A receiver as claimed in claim 19, in which said symbols are consecutive symbols.

21. (Original) A receiver as claimed in claim 1, in which said predetermined relationship is that said first and second levels are substantially equal to each other.

22. (Original) A receiver as claimed in claim 1, in which said baseband channel signal comprises in-phase and quadrature components.

23. (Original) A receiver as claimed in claim 22, in which said filtering arrangement comprises first and second adjustable low pass filters for said in-phase and quadrature components, respectively.

24. (Original) A receiver as claimed in claim 23, comprising a second measuring arrangement for measuring levels of said in-phase and quadrature components after filtering by said first and second filters, said controller being arranged to adjust at least one of said first and second filters so that said in-phase and quadrature components after filtering have substantially a same level.

25. (Original) A receiver as claimed in claim 24, comprising:  
an analog-digital conversion arrangement disposed between said filtering arrangement and said first measuring arrangement and having an output; and  
an automatic gain control arrangement responsive to said output of said analog-digital conversion arrangement for providing substantially constant average input levels to said analog-digital conversion arrangement.

26. (Original) A receiver as claimed in claim 25, comprising a gain and phase compensating arrangement for compensating in-phase and quadrature components from said analog-digital conversion arrangement.

27. (Original) A receiver as claimed in claim 26, comprising first and second digital low pass filters for filtering said in-phase and quadrature components, respectively.

28. (Original) A receiver as claimed in claim 27, in which said compensating arrangement is responsive to the components filtered by the first and second low pass digital filters.

29. (Original) A receiver as claimed in claim 27, in which said second measuring arrangement is arranged to measure levels of said components before filtering by said first and second low pass digital filters and said controller is arranged to reduce a passband of one of said first and second low pass filters corresponding to one of said components of larger level.

30. (Original) A receiver as claimed in claim 27, in which said second measuring arrangement is arranged to measure levels of said components after filtering by said first and second low pass digital filters and said controller is arranged to reduce a passband of the one of said first and second low pass filters corresponding to one of said components of smaller level.